



1st Mar 2021

EXAMINATIONS FEE NOTIFICATION

B. Tech-III Semester Regular/Supplementary (MIC18) Examinations–Mar 2021 (2018 Admitted Batches onwards)

CANDIDATES APPEARING FOR THE ABOVE EXAMINATIONS, WHICH ARE COMMENCING FROM **26-03-2021**, ARE INFORMED THAT THE EXAMINATION REGISTRATION FEE SHALL BE PAID AS PER THE SCHEDULE GIVEN BELOW.

Exam Registration	Last Date
Without Late Fee	08-03-2021
With Late Fee Rs. 100/-	12-03-2021
With Late Fee Rs. 1000/-	15-03-2021
With Late Fee Rs. 5000/- (Under Tatkal Scheme)	Before 3 days of commencement of that Exam

EXAMINATION FEE	
For Whole Semester Examination (All Subjects)	Rs. 1400/-
For one Subject (Theory/Practical)	Rs. 500/-
For Two Subjects (Theory/Practical)	Rs. 750/-
For Three Subjects (Theory/Practical)	Rs. 1000/-
Four and Above Subjects (Theory/Practical)	Rs. 1400/-

Note:

- The examination registration fee shall be paid through **exams.mictech.ac.in**.
- Mere payment of the examination fee does not guarantee eligibility for appearing for the examinations.
- Hall Tickets will be issued only to the eligible candidates.
- Laboratory examinations will be conducted before commencement of theory examinations.
- Schedules of **THEORY** and **LABORATORY** examinations will be displayed in the website soon.
- Candidates registered under **TATKAL SCHEME** shall submit the payment receipt to exam section on the same day.

CONTROLLER OF EXAMINATIONS

Copy To: 1. All HODs

2. Notice Boards

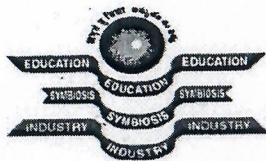
3. College website

CONTROLLER OF EXAMINATIONS
Devineni Venkata Ramana & Dr. Himasekhar
MIC COLLEGE OF TECHNOLOGY
KANCHIKACHERLA, Krishna District.

PRINCIPAL

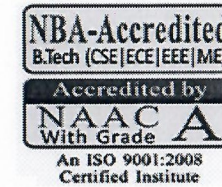
PRINCIPAL

Devineni Venkata Ramana & Dr. Himasekhar
MIC College of Technology
KANCHIKACHERLA



Devineni Venkata Ramana & Dr. Hima Sekhar
MIC College of Technology
(Autonomous)

(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)
Kanchikacherla- 521180, Krishna District, A.P., India



EXAMINATION SECTION - NOTIFICATION DATED : 01-03-2021

B.Tech (III - Semester)- Unit Test -II & Mid -II Examination Schedule- A.Y. 2020-21

Timings: FN

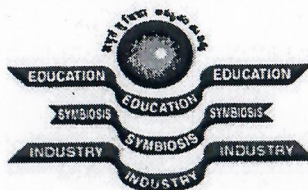
Unit Test : 9:10 TO 9:50AM

MID : 9:50 TO 11:20AM

DATE	CE	EEE	MECH	ECE	CSE	IT
08-03-2021 (Monday)	Numerical Methods and Probability Statistics (18CE3T02)	Complex Variables and Numerical Methods(18EE3T01)	Complex Variables and Numerical Methods (18ME3T01)	Probability Theory & Stochastic Processes (18EC3T01)	Probability and Statistics (18CS3T01)	Probability and Statistics (18IT3T01)
09-03-2021 (Tuesday)	Life Science (18CE3T03)	Electrical Circuit Analysis - II (18EE3T02)	Mechanics of Solids (18ME3T02)	Electronic Circuit Analysis (18EC3T02)	Object Oriented Programming (18CS3T02)	Object Oriented Programming (18IT3T02)
12-03-2021 (Friday)	Building Materials and Construction (18CE3T04)	Analog Electronics-I (18EE3T03)	Kinematics of Machines (18ME3T03)	Digital Electronics (18EC3T03)	Advanced Data Structures (18CS3T03)	Advanced Data Structures (18IT3T03)
13-03-2021 (Saturday)	Strength of Materials-I (18CE3T05)	Electrical Machines-I (18EE3T04)	Thermodynamics (18ME3T04)	Network Theory (18EC3T04)	Digital Logic Design (18CS3T04)	Digital Logic Design (18IT3T04)
15-03-2021 (Monday)	Effective Technical Communication (18CE3T01)	Electromagnetic Fields(18EE3T05)	Materials Science and Engineering (18ME3T05)	Signals & Systems (18EC3T05)	Effective Technical Communication (18CS3T05)	Effective Technical Communication (18IT3T05)
16-03-2021 (Tuesday)	Fluid Mechanics (18CE3T06)	Basics of Mechanical Engineering (18EE3T06)	Fluid Mechanics & Hydraulic Machines (18ME3T06)	Managerial Economics and Financial Analysis (18EC3T06)	-	-
17-03-2021 (Wednesday)	Surveying (18CE3T07)	-	-	-	-	-

11-8/13/21
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MIC COLLEGE OF TECHNOLOGY
KANCHIKACHERLA, Krishna District.

11/13/21
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Kanchikacherla- 521180, Krishna District, A.P., India

EXAMINATION SECTION - NOTIFICATION DATED : 01-03-2021

B.Tech (III - Semester)- Mandatory Course Examination Schedule- A.Y. 2020-21

Regulation: MIC18

Timings: FN:

9:30 TO 12:00PM

DATE	CIVIL	EEE	ECE	CSE	IT
18-03-2021 (Thursday)	Management-I (Organizational Behavior) (18CE3N10)	Education Technology & Society (18EE3T09)	Constitution of India (18EC3N09)	Indian Constitution (18CS3T06)	Indian Constitution (18IT3T06)

18/3/21
CONTROLLER OF EXAMINATIONS

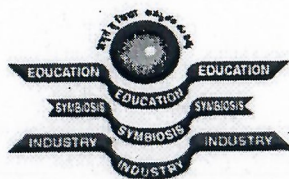
Copy to : 1.All HODs (to be read in all the B.Tech III sem classrooms)
2. Notice Board

CONTROLLER OF EXAMINATIONS
Devineni Venkata Ramana & Dr. Himasekhar
MIC COLLEGE OF TECHNOLOGY
KANCHIKACHERLA, Krishna District.

18/3/21
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Kanchikacherla- 521180, Krishna District, A.P., India

B.Tech III - Semester- Regular/Supplementary(MIC18) Examinations, Mar 2021

TIME TABLE

TIME : 10:00 AM TO 1:00PM

BRANCH	27-03-2021 (Saturday)	31-03-2021 (Wednesday)	03-04-2021 (Saturday)	07-04-2021 (Wednesday)	09-04-2021 (Friday)	12-04-2021 (Monday)	15-04-2021 (Thursday)
CIVIL ENGINEERING (01-CE)	Numerical Methods and Probability Statistics (18CE3T02)	Life Science (18CE3T03)	Building Materials and Construction (18CE3T04)	Strength of Materials-I (18CE3T05)	Effective Technical Communication (18CE3T01)	Fluid Mechanics (18CE3T06)	Surveying (18CE3T07)
ELECTRICAL AND ELECTRONICS ENGINEERING (02 - EEE)	Complex Variables and Numerical Methods (18EE3T01)	Electrical Circuit Analysis - II (18EE3T02)	Analog Electronics- I (18EE3T03)	Electrical Machines-I (18EE3T04)	Electromagnetic Fields (18EE3T05)	Basics of Mechanical Engineering (18EE3T06)	-
MECHANICAL ENGINEERING (03 - ME)	Complex Variables and Numerical Methods (18ME3T01)	Mechanics of Solids (18ME3T02)	Kinematics of Machines (18ME3T03)	Thermodynamics (18ME3T04)	Materials Science and Engineering (18ME3T05)	Fluid Mechanics & Hydraulic Machines (18ME3T06)	-
ELECTRONICS & COMMUNICATIONS ENGINEERING (04 - ECE)	Probability Theory & Stochastic Processes (18EC3T01)	Electronic Circuit Analysis (18EC3T02)	Digital Electronics (18EC3T03)	Network Theory (18EC3T04)	Signals & Systems (18EC3T05)	Managerial Economics and Financial Analysis (18EC3T06)	-
COMPUTER SCIENCE & ENGINEERING (05 - CSE)	Probability and Statistics (18CS3T01)	Object Oriented Programming (18CS3T02)	Advanced Data Structures (18CS3T03)	Digital Logic Design (18CS3T04)	Effective Technical Communication (18CS3T05)	-	-
INFORMATION TECHNOLOGY (12-IT)	Probability and Statistics (18IT3T01)	Object Oriented Programming (18IT3T02)	Advanced Data Structures (18IT3T03)	Digital Logic Design (18IT3T04)	Effective Technical Communication (18IT3T05)	-	-

NOTE:

1. ANY OMISSIONS/ CLASHES / DISCREPANCIES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE COE, IMMEDIATELY.
2. ALL THE STUDENTS ARE ADVISED TO MAKE A NOTE OF THE EXAMINATION SCHEDULE.

8/13/21
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KANCHIKACHERLA, KRISHNA DISTRICT

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