



Devineni Venkata Ramana & Dr. Hima Sekhar
MIC College of Technology
An Autonomous Institution

Date: 13-03-2021

DEPARTMENT OF CIVIL ENGINEERING

B. Tech V Sem – MIC18
Regular Lab Schedule Mar 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
19-03-2021	Engineering Geology Lab –18CE5L07	A	B	C
20-03-2021	Geotechnical Engineering Lab – 18CE5L08	A	B	C

Batch Members (70)

A: 18H71A0101 – 18H71A0128 (24)

B: 18H71A0129 – 18H71A0130,
19H75A0101 – 19H75A0123 (23)

C: 19H75A0124 – 19H75A0147 (23)



KJ
13/3/21
Chief Superintendent



Devineni Venkata Ramana & Dr. Hima Sekhar
MIC College of Technology
An Autonomous Institution

Date: 13-03-2021

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

B. Tech V Sem – MIC18
Regular Lab Schedule Mar 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
18-03-2021	Electrical Machines – II Lab – 18EE5L08	A	B	C
19-03-2021	Electrical Measurements Lab – 18EE5L09	A	B	C
20-03-2021	Python Programming Lab – 18EE5L10	A	B	C

Batch Members (74)

A: 18H71A0201 – 18H71A0220 (25)
19H75A0201 – 19H75A0206
B: 19H75A0207 – 19H75A0232 (25)
C: 19H75A0233 – 19H75A0257 (24)



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Date: 13-03-2021

DEPARTMENT OF MECHANICAL ENGINEERING

B. Tech V Sem – MIC18
Regular Lab Schedule Mar 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
18-03-2021	Machine Tools Lab – 18ME5L09	A	B	C
19-03-2021	Theory of Machines Lab – 18ME5L08	A	B	C
20-03-2021	Communication Skills Lab -18ME5L10	A	B	C

Batch Members (84)

A: 18H71A0301–18H71A0329 (28)

B: 18H71A0330–18H71A0346

19H75A0301 – 19H75A0313 (28)

C: 19H75A0314 – 19H75A0343 (28)



K 13/3/21
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Date: 13-03-2021

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

B. Tech V Sem – MIC18
Regular Lab Schedule Mar 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
18-03-2021	Microcontroller and Microprocessors Lab –18EC5L08	A	B	C
	Digital Signal Processing Lab – 18EC5L09	D	E	F
	Communication Systems Lab - 18EC5L10	G	H	I
19-03-2021	Microcontroller and Microprocessors Lab –18EC5L08	D	E	F
	Digital Signal Processing Lab – 18EC5L09	G	H	I
	Communication Systems Lab - 18EC5L10	A	B	C
20-03-2021	Microcontroller and Microprocessors Lab –18EC5L08	G	H	I
	Digital Signal Processing Lab – 18EC5L09	A	B	C
	Communication Systems Lab - 18EC5L10	D	E	F

Batch Members (185)

A:18H71A0401–18H71A0421	(21)
B: 18H71A0422–18H71A0442	(21)
C: 18H71A0443–18H71A0464	(20)
D: 18H71A0465–18H71A0485	(21)
E: 18H71A0486–18H71A04A7	(21)
F: 18H71A04A8–18H71A04C9	(21)
G: 18H71A04D0–18H71A04F3	(20)
H: 18H71A04F4–18H71A04H7	(20)
I : 18H71A04H8–18H71A04I0, 19H75A0401 – 19H75A0418	(20)



K/13/3/21
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Date: 13-03-2021

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
B. Tech V Sem – MIC18

Regular Lab Schedule Mar 2021

Date	Name of the Lab	09:00 – 12:00	01:00 – 04:00
17-03-2021	Data Mining Lab -18CS5L16	A	B
	Web Technologies Lab – 18CS5L17	C	D
18-03-2021	Data Mining Lab -18CS5L16	C	D
	Web Technologies Lab – 18CS5L17	A	B

Batch Members (182)

- A: 18H71A0501 – 18H71A0546 (45)
B: 18H71A0547 – 18H71A0591 (45)
C: 18H71A0592 – 18H71A05E1 (46)
D: 18H71A05E2 – 18H71A05IO, 19H75A0501 – 19H75A0507 (46)




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Date: 13-03-2021

DEPARTMENT OF INFORMATION TECHNOLOGY

B. Tech V Sem – MIC18

Regular Lab Schedule Mar 2021

Date	Name of the Lab	09:00 – 12:00	01:00 – 04:00
17-03-2021	Data Mining Lab - 18IT5L16	A	B
18-03-2021	Web Technologies Lab – 18IT5L17	A	B

Batch Members (53)

A: 18H71A1201 – 18H71A1228

(27)

B: 18H71A1229 – 18H71A1257

(26)




Chief Superintendent