



Devineni Venkata Ramana & Dr. Hima Sekhar

MIC College of Technology

(Autonomous)

(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)

Kanchikacherla- 521180, Krishna District, A.P., India

B.Tech VI - Semester- Cycle -1 & Cycle-2 (MIC18) Examinations, August 2021

TIME TABLE

TIME : UNIT-I 9:30 AM TO 10:10 AM

MID-I 10:10 AM to 11:40 AM

TIME : UNIT-II 1:30 PM TO 02:10 PM

MID-II 02:10 PM to 03:40 PM

BRANCH	02-08-2021 (Monday)	03-08-2021 (Tuesday)	04-08-2021 (Wednesday)	05-08-2021 (Thursday)	06-08-2021 (Friday)	07-08-2021 (Saturday)
CIVIL (01-CE)	Structural Engineering - II (Steel) (18CE6T01)	Environmental Engineering-I (18CE6T02)	Structural Analysis-II (18CE6T03)	Transportation Engineering-II (18CE6T04)	Quantitative Aptitude & Reasoning (18CE6T05)	Low Cost Housing (18CE6T06)
EEE (02-EEE)	Microprocessors and Microcontrollers (18EE6T01)	Switchgear and Protection (18EE6T02)	Management Science (18EE6T04)	OOPS through JAVA (18EE6T03)	Energy Audit Conservation Management (18EE6T08)	--
MECH (03-MECH)	Design of Transmission Elements (18ME6T01)	Heat Transfer (18ME6T02)	Metrology and measurements (18ME6T03)	Refrigeration & Air Conditioning (18ME6T06)	Nanotechnology (18ME6T14)	--
ECE (04-ECE)	VLSI Design (18EC6T01)	Antennas & Wave Propagation (18EC6T02)	Cellular & Mobile Communications (18EC6T03)	OOPS through JAVA (18EC6T06)	Embedded Systems (18EC6T09)	--
CSE (05-CSE)	Compiler Design (18CS6T01)	Computer Networks (18CS6T02)	Software Engineering (18CS6T03)	UNIX & Shell Programming (18CS6T04)	Quantitative Aptitude & Reasoning (18CS6T07)	--
IT (12-IT)	Compiler Design (18IT6T01)	Computer Networks (18IT6T02)	Software Engineering (18IT6T03)	UML & Design Patterns (18IT6T05)	Quantitative Aptitude & Reasoning (18IT6T07)	--

Copy to : 1.To All HODS (to be read in all the B.Tech VI sem classrooms)
2. Notice Board

CONTROLLER OF EXAMINATIONS

DATE: 20-07-2021



KJ
20/7/21

PRINCIPAL

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Devineni Venkata Ramana & Dr. Himasekhar
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