



Devineni Venkata Ramana & Dr. Hima Sekhar
MIC College of Technology
An Autonomous Institution

Date: 16-08-2021

DEPARTMENT OF CIVIL ENGINEERING

B. Tech VI Sem – MIC18

Regular Lab Schedule July 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
21-08-2021	Architectural Planning and CAD Lab – 18CE6L07	A	B	C
23-08-2021	Transportation Engineering Lab – 18CE6L08	A	B	C

Batch Members (69)

- A: 18H71A0101 – 18H71A0128 (23)
- B: 18H71A0129 – 18H71A0130,
19H75A0101 – 19H75A0123 (23)
- C: 19H75A0124 – 19H75A0147 (23)



KJ
16/8/21
Chief Superintendent
CHIEF SUPERINTENDENT
DVR & Dr. HS MIC College of Technology
Kanchikacherla, Krishna Dt.
Andhra Pradesh, India – Pin: 521180.



Devineni Venkata Ramana & Dr. Hima Sekhar
MIC College of Technology
An Autonomous Institution

Date: 16-08-2021

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

B. Tech VI Sem – MIC18

Regular Lab Schedule July 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
20-08-2021	Microprocessors and Micro Controllers Lab – 18EE6L07	A	B	C
21-08-2021	Power Electronics & Simulation Lab – 18EE6L08	A	B	C
23-08-2021	OOPS through JAVA Lab – 18EE6L09	A	B	C

Batch Members (73)

- A: 18H71A0201 – 18H71A0220 (25)
19H75A0201 – 19H75A0206
B: 19H75A0207 – 19H75A0232 (25)
C: 19H75A0233 – 19H75A0257 (23)



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Date: 16-08-2021

DEPARTMENT OF MECHANICAL ENGINEERING

B. Tech VI Sem – MIC18

Revised Regular Lab Schedule July 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
20-08-2021	Heat Transfer Lab – 18ME6L22	A	B	C
21-08-2021	Metrology & Instrumentation Lab – 18ME6L21	A	B	C

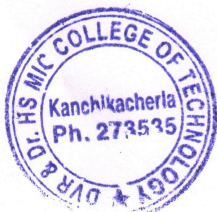
Batch Members (81)

A: 18H71A0301–18H71A0329 (27)

B: 18H71A0330–18H71A0346

19H75A0301 – 19H75A0313 (27)

C: 19H75A0314 – 19H75A0343 (27)



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Date: 16-08-2021

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

B. Tech VI Sem – MIC18

Regular Lab Schedule July 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
20-08-2021	VLSI LAB –18EC6L10	A	B	C
	OOPS Through JAVA Lab – 18EC6L11	D	E	F
21-08-2021	VLSI LAB –18EC6L10	G	H	I
	OOPS Through JAVA Lab – 18EC6L11	A	B	C
23-08-2021	VLSI LAB –18EC6L10	D	E	F
	OOPS Through JAVA Lab – 18EC6L11	G	H	I

Batch Members (185)

A: 18H71A0401–18H71A0421	(21)
B: 18H71A0422–18H71A0442	(21)
C: 18H71A0443–18H71A0464	(20)
D: 18H71A0465–18H71A0485	(21)
E: 18H71A0486–18H71A04A7	(21)
F: 18H71A04A8–18H71A04C9	(21)
G: 18H71A04D0–18H71A04F3	(20)
H: 18H71A04F4–18H71A04H7	(20)
I: 18H71A04H8–18H71A04I0, 19H75A0401 – 19H75A0418	(20)



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Date: 16-08-2021

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

B. Tech VI Sem – MIC18

Regular Lab Schedule July 2021

Date	Name of the Lab	09:00 – 12:00	01:00 – 04:00
20-08-2021	Computer Networks Lab - 18CS6L21	A	B
	Software Engineering Lab – 18CS6L22	C	D
21-08-2021	Computer Networks Lab - 18CS6L21	C	D
	Software Engineering Lab – 18CS6L22	A	B

Batch Members (182)

A: 18H71A0501 – 18H71A0546 (45)

B: 18H71A0547 – 18H71A0591 (45)

C: 18H71A0592 – 18H71A05E1 (46)

D: 18H71A05E2 – 18H71A05IO, 19H75A0501 – 19H75A0507 (46)




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Date: 16-08-2021

DEPARTMENT OF INFORMATION TECHNOLOGY

B. Tech VI Sem – MIC18

Regular Lab Schedule July 2021

Date	Name of the Lab	09:00 – 12:00	01:00 – 04:00
20-08-2021	Computer Networks Lab -18IT6L21	A	B
21-08-2021	Software Engineering Lab – 18IT6L22	A	B

Batch Members (53)

A: 18H71A1201 – 18H71A1228 (27)

B: 18H71A1229 – 18H71A1257 (26)



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