



Devineni Venkata Ramana & Dr. Hima Sekhar
MIC College of Technology
An Autonomous Institution

Date: 23-08-2021

DEPARTMENT OF CIVIL ENGINEERING

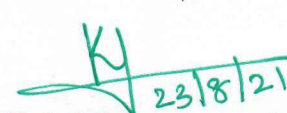
B. Tech IV Sem – MIC18
Regular/Supple. Lab Schedule Aug 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
25-08-2021	Surveying Field Work–II–18CE4L07	A	B	C
26-08-2021	Concrete Technology Lab – 18CE4L08	A	B	C
27-08-2021	Fluid Mechanics and Hydraulic Machinery Lab-18CE4L09	A	B	C

Batch Members (28)

A: 19H71A0101 – 19H71A0110, 20H75A0102 TO 120




Chief Superintendent

CHIEF SUPERINTENDENT
DVR & Dr. HS MIC College of Technology
Kanchikacherla, Krishna Dt.
Andhra Pradesh, India – Pin: 521180.



Devineni Venkata Ramana & Dr. Hima Sekhar
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Date: 23-08-2021

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

B. Tech IV Sem – MIC18
Regular/Supple. Lab Schedule Aug 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
25-08-2021	Control Systems & Simulation Lab – 18EE4L07	A	B	C
26-08-2021	Electrical Machines – I Lab – 18EE4L08	A	B	C
27-08-2021	Data Structures Through C Lab– 18EE4L09	A	B	C

Batch Members (82)

A: 19H71A0201 – 19H71A0220 (28)

20H75A0201 – 20H75A0209

B: 20H75A0210 – 20H75A0238 (28)

C: 20H75A0239 – 20H75A0265 (26)




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Date: 23-08-2021

DEPARTMENT OF MECHANICAL ENGINEERING

B. Tech IV Sem – MIC18
Regular/Supple. Lab Schedule Aug 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
25-08-2021	Computer Aided Machine Drawing – 18ME4T05	A	B	C
26-08-2021	Production Technology Lab – 18ME4L09	A	B	C
27-08-2021	Thermal Engineering Lab -8ME4L10	A	B	C

Batch Members (40)

A: 19H71A0301–19H71A0313 ,20H75A0301 TO 308 (20)

B: 20H75A0309–20H75A0328 (20)




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Date: 23-08-2021

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

B. Tech IV Sem – MIC18
Regular/Supple. Lab Schedule Aug 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
25-08-2021	Analog IC Applications Lab –18EC4L07	A	B	C
	Digital System Design Using HDL Lab –18EC4L08	D	E	F
	Mini Project-18EC4L09	G	H	I
26-08-2021	Analog IC Applications Lab –18EC4L07	D	E	F
	Digital System Design Using HDL Lab –18EC4L08	G	H	I
	Mini Project-18EC4L09	A	B	C
27-08-2021	Analog IC Applications Lab –18EC4L07	G	H	I
	Digital System Design Using HDL Lab –18EC4L08	A	B	C
	Mini Project-18EC4L09	D	E	F

Batch Members (196)

A:19H71A0401–19H71A0419,18-4E0,4G3,4H5	(22)
B: 19H71A0420–19H71A0441	(22)
C: 19H71A0442–19H71A0464	(22)
D: 19H71A0465–19H71A0487	(22)
E: 19H71A0488–19H71A04A9	(22)
F: 19H71A04B0–19H71A04D2	(22)
G: 19H71A04D3–19H71A04F4	(22)
H: 19H71A04F5–19H71A04G8,20H75A0401 TO 408	(22)
I : 20H75A0409 -20H75A0428	(20)



KJ
23/8/21

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Devineni Venkata Ramana & Dr. Hima Sekhar
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Date: 23-08-2021

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
B. Tech IV Sem – MIC18
Regular/Supple. Lab Schedule Aug 2021

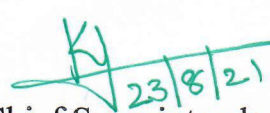
Date	Name of the Lab	09:00 – 12:00	01:00 – 04:00
25-08-2021	Operating Systems & LINUX Programming Lab -18CS4L07	A	B
	Database Management Systems Lab – 18CS4L08	C	D
	Python Programming Lab – 18CS4L09	E	F
26-08-2021	Operating Systems & LINUX Programming Lab -18CS4L07	C	D
	Database Management Systems Lab – 18CS4L08	E	F
	Python Programming Lab – 18CS4L09	A	B
27-08-2021	Operating Systems & LINUX Programming Lab -18CS4L07	E	F
	Database Management Systems Lab – 18CS4L08	A	B
	Python Programming Lab – 18CS4L09	C	D

Batch Members (188)

- A: 19H71A0501 – 19H71A0532 (31)
B: 19H71A0533 – 19H71A0564 (31)
C: 19H71A0565 – 19H71A0595 (31)
D: 19H71A0596– 19H71A05C6 (31)
E: 19H71A05C7 – 19H71A05F7 (31)
F: 19H71A05F8 – 19H71A05H2, 20H75A0501 – 20H75A0518 (33)

& Suppl 18H71A0569(DBMS Lab)




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Date: 23-08-2021

DEPARTMENT OF INFORMATION TECHNOLOGY

B. Tech IV Sem – MIC18
Regular/Supple. Lab Schedule Aug 2021

Date	Name of the Lab	09:00 – 12:00	01:00 – 04:00
25-08-2021	Operating Systems & LINUX Programming Lab - 18IT4L07	A	B
26-08-2021	Database Management Systems Lab – 18IT4L08	A	B
27-08-2021	Python Programming Lab – 18IT4L09	A	B

Batch Members (66)

A: 19H71A1201 – 19H71A1231, 18H71A1249, 1256 (33)

B: 19H71A1232 – 19H71A1264, 20H75A1201 (33)




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