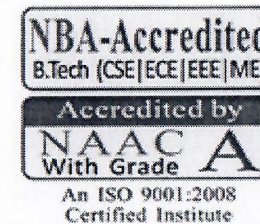


Devineni Venkata Ramana & Dr. Hima Sekhar
MIC College of Technology
(Autonomous)

(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)
Kanchikacherla- 521180, Krishna District, A.P., India



M.Tech II - Semester- Supplementary (MIC18) Examinations, Sep-2021

TIME TABLE

Timings: FN: 10:00 AM TO 1:00 PM

DATE & DAY	18-10-2021 (Monday)	20-10-2021 (Wednesday)	22-10-2021 (Friday)	25-10-2021 (Monday)	27-10-2021 (Wednesday)
Machine Design (Mech-31)	Finite Element Method (18312T01)	Computer Aided Design (18312T02)	Program Elective 3 Robotics (18312T04)	Program Elective 4 Optimization Techniques in Design (18312T08)	—
VSLI & ES (ECE-41)	Embedded System Design (18412T01)	Low Power VLSI Design (18412T02)	Program Elective 3 CPLD&FPGA Architectures and Applications (18412T05)	Program Elective 4 DSP Processors & Architectures (18412T07)	Intellectual Property Rights And Patents (18412T11)

NOTE:

1. ANY OMISSIONS/ CLASHES / DISCREPANCIES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE COE, IMMEDIATELY.
2. ALL THE STUDENTS ARE ADVISED TO MAKE A NOTE OF THE EXAMINATION SCHEDULE.

18/9/21
CONTROLLER OF EXAMINATIONS
DATE: 18-09-2021



18/9/21
PRINCIPAL
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Devineni Venkata Ramana & Dr. Himasekhar
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