

Devineni Venkata Ramana & Dr. Hima Sekhar MIC College of Technology An Autonomous Institution

Date: 21-09-2021

External Lab (Supple) Schedule Aug 2021

Branch	Date	Name of the Lab	9:30- 12:30	01:00– 04:00
CIVIL	24-09-2021	Engineering Geology Lab – 18CE5L07	ALL	-
MECH	24-09-2021	Communication Skills Lab - 18ME5L10	ALL	-
ECE	24-09-2021	Microcontroller and Microprocessors Lab – 18EC5L08	ALL	-
	24-09-2021	Digital Signal Processing Lab – 18EC5L09	ALL	-
	24-09-2021	Communication Systems Lab - 18EC5L10	-	ALL

B.Tech V Sem Supple. – MIC18

Mandatory Course (Supple) Schedule Aug 2021

Branch Date		Name of the Lab	9:30-12:30
MECH	25-09-2021	Essence of Indian Traditional Culture 18ME5T11	ALL



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Chief Superintendent

CHIEF SUPERINTENDENT DVR & Dr. HS MIC College of Technolog Kanchikacherla, Krishna Dt. Andhra Pradesti, India – Pin: 521180.



Devineni Venkata Ramana & Dr. Hima Sekhar MIC College of Technology

An Autonomous Institution

B.Tech V SEM Supple . - MIC18

	REGULATIO		SE		
REGNO	N	GRP	M	PCODE	PNAME
18H71A0109	MIC18	CIVIL	5	18CE5L07	Engineering Geology Lab
					Microcontroller and Microprocessors
18H71A0443	MIC18	ECE	5	18EC5L08	Lab
					Microcontroller and Microprocessors
18H71A0466	MIC18	ECE	5	18EC5L08	Lab
18H71A0411	MIC18	ECE	5	18EC5L09	Digital Signal Processing Lab
18H71A0425	MIC18	ECE	5	18EC5L09	Digital Signal Processing Lab
18H71A0468	MIC18	ECE	5	18EC5L09	Digital Signal Processing Lab
18H71A0474	MIC18	ECE	5	18EC5L09	Digital Signal Processing Lab
18H71A04H0	MIC18	ECE	5	18EC5L09	Digital Signal Processing Lab
18H71A04B5	MIC18	ECE	5	18EC5L10	Communication Systems Lab
18H71A04F4	MIC18	ECE	5	18EC5L10	Communication Systems Lab
18H71A04H0	MIC18	ECE	5	18EC5L10	Communication Systems Lab
19H75A0328	MIC18	MECH	5	18ME5L10	Communication Skills Lab
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 18ME5T11
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Chief Superintendent



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