



DVR & Dr. HS

MIC College of Technology

An Autonomous Institution

Approved by AICTE, Permanently Affiliated to JNTUK, Kakimada
Kanchikacherla-521180, Krishna Dt. A.P. India



Date: 30-12-2021

DEPARTMENT OF CIVIL ENGINEERING

B. Tech V Sem – MIC18

Regular / Supple Lab Schedule Dec 2021

Date	Name of the Lab	09:00–12:00
10-01-2022	Engineering Geology Lab –18CE5L07	A
11-01-2022	Geotechnical Engineering Lab – 18CE5L08	A

Batch Members (28)

A: 19H71A0101 – 19H71A0110, 20H75A0102 TO 120

& All Supple. candidates




Chief Superintendent

CHIEF SUPERINTENDENT
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Date: 30-12-2021

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

B. Tech V Sem – MIC18

Regular / Supple Lab Schedule Dec 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
08-01-2022	Electrical Machines – II Lab – 18EE5L08	A	B	C
10-01-2022	Electrical Measurements Lab – 18EE5L09	A	B	C
11-01-2022	Python Programming Lab – 18EE5L10	A	B	C

Batch Members (81)

A: 19H71A0201 – 19H71A0220 (27)

20H75A0201 – 20H75A0208

B: 20H75A0209 – 20H75A0237 (27)

C: 20H75A0238 – 20H75A0265 (27)

& All Supple. candidates



KY 5/11/22

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Date: 30-12-2021

DEPARTMENT OF MECHANICAL ENGINEERING

B. Tech V Sem – MIC18

Regular / Supple Lab Schedule Dec 2021

Date	Name of the Lab	09:00–12:00	11:00–02:00
08-01-2022	Machine Tools Lab – 18ME5L09	A	B
10-01-2022	Theory of Machines Lab – 18ME5L08	A	B
11-01-2022	Communication Skills Lab - 18ME5L10	A	B

Batch Members (39)

A: 19H71A0301–19H71A0313 , 20H75A0301 TO 308 (20)

B: 20H75A0309–20H75A0328 (19)

& All Supple. candidates



KY 31/12/22

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**Date: 30-12-2021**

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

B. Tech V Sem – MIC18**Regular / Supple Lab Schedule Dec 2021**

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
08-01-2022	Microcontroller and Microprocessors Lab – 18EC5L08	A	B	C
	Digital Signal Processing Lab –18EC5L09	D	E	F
	Communication Systems Lab -18EC5L10	G	H	I
10-01-2022	Microcontroller and Microprocessors Lab – 18EC5L08	D	E	F
	Digital Signal Processing Lab –18EC5L09	G	H	I
	Communication Systems Lab -18EC5L10	A	B	C
11-01-2022	Microcontroller and Microprocessors Lab – 18EC5L08	G	H	I
	Digital Signal Processing Lab –18EC5L09	A	B	C
	Communication Systems Lab -18EC5L10	D	E	F

Batch Members (192)

A: 19H71A0401–19H71A0420,18-4G3,4H5	(22)
B: 19H71A0421–19H71A0442	(22)
C: 19H71A0443–19H71A0465	(22)
D: 19H71A0466–19H71A0488	(22)
E: 19H71A0489–19H71A04B1	(22)
F: 19H71A04B2–19H71A04D5	(22)
G: 19H71A04D6–19H71A04F5	(20)
H: 19H71A04F6–19H71A04G8, 20H75A0401 TO 408	(20)
I: 20H75A0409 -20H75A0428 & All Supple. candidates	(20)




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Date: 30-12-2021

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

B. Tech V Sem – MIC18

Regular / Supple Lab Schedule Dec 2021

Date	Name of the Lab	09:00 – 12:00	01:00 – 04:00
10-01-2022	Data Mining Lab - 18CS5L16	A	B
	Web Technologies Lab - 18CS5L17	C	D
11-01-2022	Data Mining Lab - 18CS5L16	C	D
	Web Technologies Lab - 18CS5L17	A	B

Batch Members (187)

A: 19H71A0501 – 19H71A0549 (47)

B: 19H71A0550 – 19H71A0596 (47)

C: 19H71A0597 – 19H71A05E4 (47)

D: 19H71A05E5– 19H71A05H2, (46)

–20H75A0501 – 20H75A0518 & All Supple. Candidates




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Date: 30-12-2021

DEPARTMENT OF INFORMATION TECHNOLOGY

B. Tech V Sem – MIC18

Regular / Supple Lab Schedule Dec 2021

Date	Name of the Lab	09:00 – 12:00	01:00 – 04:00
10-01-2022	Data Mining Lab - 18IT5L16	A	B
11-01-2022	Web Technologies Lab – 18IT5L17	A	B

Batch Members (66)

A: 19H71A1201 – 19H71A1231, 18H71A1249, 1256 (33)

B: 19H71A1232 – 19H71A1264, 20H75A1201 (33)




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