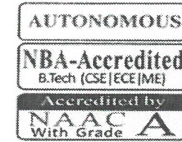


Devineni Venkata Ramana & Dr. Hima Sekhar
MIC College of Technology
(Autonomous)

(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)
Kanchikacherla- 521180, Krishna District, A.P., India



EXAMINATION SECTION - NOTIFICATION DATED : 22-04-2022

External Practical Exam schedule for M. Tech I Sem Regular (MIC20)- Apr 2022

TIME : 10:00 AM TO 1:00PM

DATE	VLSI&ES	CSE
	NAME OF THE LABORATORY	NAME OF THE LABORATORY
26-04-2022 (Tuesday)	VLSI SYSTEM DESIGN LAB-I (20411L10)	ADVANCED DATA STRUCTURES & ALGORITHMS LAB (20511L10)
27-04-2022 (Wednesday)	DIGITAL SYSTEM DESIGN USING VERILOG AND VHDL (20411L11)	ADVANCED COMPUTING LAB (20511L11)

M. S. 22/4
CONTROLLER OF EXAMINATIONS

The Controller of Examinations
DVR & Dr HS MIC College of Technology
Kanchikacherla - 521180
Copy to : 1. To HOD, ECE/CSE (to be read in the M.Tech I sem class room)
2. Notice Board



KJ 22/4/22
PRINCIPAL

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DVR & Dr. HS MIC College of Technology
Kanchikacherla, Krishna Dt.
Andhra Pradesh, India - Pin. 521180.