



Devineni Venkata Ramana & Dr. Hima Sekhar  
**MIC College of Technology**  
An Autonomous Institution

Date: 19-09-2022

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
**External Lab (Regular) Schedule Sep 2022**

**M.Tech II Semester – MIC20**

Date	Name of the Lab	09:00 – 12:00	02:00 – 4:00
01-10-2022	Embedded System Design lab - 20412L10	ALL	–
01-10-2022	VLSI System design Lab-II - 20412L11	–	ALL

**Batch Members (2)**

A: 21H71D6801 – 21H71D6802



*Ky*  
21/9/22  
Chief Superintendent

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING  
**External Lab (Regular) Schedule Sep 2022**

**M.Tech II Semester – MIC20**

Date	Name of the Lab	09:00 – 12:00
29-09-2022	Machine Learning with Python Lab- 20512L09	ALL
30-09-2022	MEAN Stack Technologies Lab- 20512L10	ALL

**Batch Members (2)**

A: 21H71D5801 – 21H71D5802



*Ky*  
21/9/22  
Chief Superintendent

CHIEF SUPERINTENDENT  
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