



DVR & Dr HS

MIC College of Technology

(AUTONOMOUS)

Date: 22-04-2024


B.Tech V Sem MIC20 (Supple) Lab Schedule April 2024

Branch	Date	Name of the Lab	9:30-12:30
EEE	02-05-2024	JAVA Programming-20EE5S12	ALL
	03-05-2024	Summer Internship-20EE5I14	ALL
ECE	30-04-2024	Digital System Design using VHDL & Verilog LAB-20EC5L11	ALL
	01-05-2024	Micro Processors and Micro Controllers LAB-20EC5L10	ALL
	02-05-2024	JAVA Programming -20EC5L10	ALL
CSE	30-04-2024	Data Warehousing and Mining LAB-Through Python-20CS5L10	ALL
	01-05-2024	Mobile Application Development Lab-20CS5S12	ALL
	02-05-2024	Design and Analysis of Algorithms LAB-20CS5L11	ALL
	03-05-2024	Summer Internship-20CS5I14	ALL
AIDS	30-04-2024	Testing Tools Lab- 20AD5S10	ALL
	01-05-2024	Machine Learning Lab-20AD5L08	ALL
	02-05-2024	Data Warehousing and Mining Lab-20AD5L09	ALL
	03-05-2024	Summer Internship-20AD5I12	ALL

Mandatory Course –MIC20 (Supple) Schedule April 2024

Branch	Date	Name of the Lab	9:30-12:30
ECE	29-04-2024	Essence of Indian Traditional knowledge-20EC5M13	ALL
CSE	29-04-2024	Disaster Management -20CS5M13	ALL
AIDS	29-04-2024	Disaster Management -20AD5M11	ALL




Chief Superintendent
CHIEF SUPERINTENDENT
DVR & Dr. HS MIC College of Technology
Kanchikacherla, NTR Dt.
Andhra Pradesh, India - 521180.



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(AUTONOMOUS)

V sem suppl Apr 2024

REGNO	branch	PCODE	PNAME
21H71A5401	AIDS	20AD5I12	Summer Internship
21H71A5401	AIDS	20AD5L08	Machine Learning Lab
21H71A5420	AIDS	20AD5L08	Machine Learning Lab
21H71A5423	AIDS	20AD5L08	Machine Learning Lab
21H71A5401	AIDS	20AD5L09	Data Warehousing and Mining Lab
21H71A5420	AIDS	20AD5L09	Data Warehousing and Mining Lab
21H71A5430	AIDS	20AD5L09	Data Warehousing and Mining Lab
21H71A5445	AIDS	20AD5L09	Data Warehousing and Mining Lab
21H71A5437	AIDS	20AD5M11	Disaster Management
21H71A5401	AIDS	20AD5S10	Testing Tools Lab
21H71A0514	CSE	20CS5I14	Summer Internship
21H71A0522	CSE	20CS5I14	Summer Internship
21H71A0555	CSE	20CS5I14	Summer Internship
22H75A0507	CSE	20CS5I14	Summer Internship
21H71A0587	CSE	20CS5L10	Data Warehousing and Mining Lab Through Python
21H71A05D0	CSE	20CS5L10	Data Warehousing and Mining Lab Through Python
22H75A0507	CSE	20CS5L10	Data Warehousing and Mining Lab Through Python
21H71A0587	CSE	20CS5L11	Design and Analysis of Algorithms Lab
21H71A05D0	CSE	20CS5L11	Design and Analysis of Algorithms Lab
22H75A0507	CSE	20CS5L11	Design and Analysis of Algorithms Lab
22H75A0507	CSE	20CS5M13	Disaster Management
21H71A0522	CSE	20CS5S12	Mobile Application Development
21H71A0587	CSE	20CS5S12	Mobile Application Development
21H71A05D0	CSE	20CS5S12	Mobile Application Development
22H75A0507	CSE	20CS5S12	Mobile Application Development
21H71A0422	ECE	20EC5L10	Microprocessors and Microcontrollers Lab
21H71A0439	ECE	20EC5L10	Microprocessors and Microcontrollers Lab
21H71A0446	ECE	20EC5L10	Microprocessors and Microcontrollers Lab
21H71A0461	ECE	20EC5L10	Microprocessors and Microcontrollers Lab
21H71A04A1	ECE	20EC5L10	Microprocessors and Microcontrollers Lab
21H71A04D4	ECE	20EC5L10	Microprocessors and Microcontrollers Lab
21H71A04G4	ECE	20EC5L10	Microprocessors and Microcontrollers Lab
22H75A0438	ECE	20EC5L10	Microprocessors and Microcontrollers Lab
21H71A0404	ECE	20EC5L11	Digital system Design using VHDL and Verilog Lab
21H71A0445	ECE	20EC5L11	Digital system Design using VHDL and Verilog Lab
21H71A0453	ECE	20EC5L11	Digital system Design using VHDL and Verilog Lab
21H71A0461	ECE	20EC5L11	Digital system Design using VHDL and Verilog Lab
21H71A04G1	ECE	20EC5L11	Digital system Design using VHDL and Verilog Lab
21H71A04G4	ECE	20EC5L11	Digital system Design using VHDL and Verilog Lab
21H71A0406	ECE	20EC5M13	Essence of Indian Traditional knowledge
21H71A0444	ECE	20EC5S12	JAVA Programming
20H71A0201	EEE	20EE5I14	Summer Internship
22H75A0208	EEE	20EE5S12	JAVA Programming
22H75A0217	EEE	20EE5S12	JAVA Programming