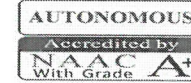




DVR & Dr. HS  
**MIC College of Technology**  
ISO 9001:2015 Certified Institute  
(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)  
Kanchikacherla - 521180, NTR Dist., A.P., India  
Phones : 08678 - 273535 / 94914 57799 / 73826 16824  
E mail: office@mictech.ac.in, Website: www.mictech.edu.in



**EXAMINATION SECTION - NOTIFICATION DATED : 20-11-2024**

**M Tech ( I - Semester)- I Mid Examination Schedule(MIC24)- A.Y. 2024-25**

**2024 Admitted Batch**

Timings: FN: 02:00PM To 04:00PM

DATE & DAY	25-11-2024 (Monday)	26-11-2024 (Tuesday)	27-11-2024 (Wednesday)	28-11-2024 (Thursday)	29-11-2024 (Friday)	30-11-2024 (Saturday)
EEE (Power Electronics & Drives)	Mathematical Modeling of Machines (24211T01)	Analysis of Power Electronics (24211T02)	Power Electronic Controls of DC Drives (24211T03)	Digital Signal Processing (24211T04)	Design Control Drives (24211T06)	Research Methodology and IPR (24211T05)
ECE (VLSI & Embedded Systems)	Digital System Design (24411T01)	Embedded System Design (24411T02)	Analog VLSI Design (24411T03)	<u>Programme Elective I</u> System on Chip Design (24411T06)	<u>Programme Elective II</u> Soft Computing Techniques (24411T08)	Research Methodology IPR (24411T12)
Computer Science & Engineering (CSE)	Mathematical Foundations of Computer Science (24511T01)	Advanced Data structures & Algorithms (24511T02)	Machine Learning (24511T03)	Big Data Analytics (24511T04)	Object Oriented Software Engineering (24511T09)	Research Methodology and IPR (24511T10)

*K. S. Sridhar*

**CONTROLLER OF EXAMINATIONS**

Copy to : 1.HOD-EEE,ECE &CSE (to be read in the M.Tech I sem classroom)  
2. Notice Board

The Controller of Examinations  
DVR & Dr HS MIC College of Technology  
Kanchikacherla - 521180 -



*[Signature]*  
**PRINCIPAL**

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DVR & Dr. HS MIC College of Technology  
Kanchikacherla, N.T.R. Dt.  
Andhra Pradesh, India – Pin: 521180.