



M.Tech I - Semester- Supplementary (MIC20) Examinations, Feb-2025

TIME TABLE

TIME : 10:00 AM TO 1:00PM

DATE & DAY	03-02-2025 (Monday)	05-02-2025 (Wednesday)	07-02-2025 (Friday)	10-02-2025 (Monday)	12-02-2025 (Wednesday)	14-02-2025 (Friday)
Power Electronics and Drives (EEE)	Mathematical Modeling of Machines (20211T01)	Analysis Of Power Electronic Converters (20211T02)	Power Electronic Control Of Dc Drives (20211T03)	Digital Signal Processing (20211T04)	Research Methodology & IPR (20211T05)	-
Machine Design (MECH)	Advanced Stress Analysis (20311T01)	Advanced Vibrations and Acoustics (20311T02)	Advanced Machine Design (20311T03)	Advanced Engineering Materials (20311T06)	Research Methodology & IPR (20311T09)	-
M.Tech VLSI & Embedded Systems (ECE)	Digital System Design (20411T01)	Embedded System Design (20411T02)	Analog VLSI Design (20411T03)	Programme Elective I System on Chip Design (20411T06)	Programme Elective II Network Security and Cryptography (20411T12)	Research Methodology (20411T12)
Computer Science and Engineering (CSE)	Mathematical Foundations of Computer Science (20511T01)	Advanced Data structures & Algorithms (20511T02)	Big Data Analytics (20511T03)	Object Oriented Software Engineering (20511T08)	Research Methodology and IPR (20511T09)	-

NOTE:

1. ANY OMISSIONS/ CLASHES / DISCREPANCIES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE COE, IMMEDIATELY.
2. ALL THE STUDENTS ARE ADVISED TO MAKE A NOTE OF THE EXAMINATION SCHEDULE.

CONTROLLER OF EXAMINATIONS
DATE:02-01-2025

The Controller of Examinations
DVR & Dr HS MIC College of Technology
Kanchikacherla - 521180



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