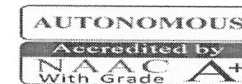




DVR & Dr. HS  
**MIC College of Technology**

ISO 9001:2015 Certified Institute  
 (Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)  
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## M.Tech I - Semester- Regular (MIC24) Examinations, Feb 2025

### TIME TABLE

**TIME : 10:00 AM TO 1:00PM**

BRANCH	03-02-2025 (Monday)	05-02-2025 (Wednesday)	07-02-2025 (Friday)	10-02-2025 (Monday)	12-02-2025 (Wednesday)	14-02-2025 (Friday)
M.Tech Electrical & Electronics Engineering (Power Electronics & Drives)	Mathematical Modeling of Machines (24211T01)	Analysis of Power Electronics (24211T02)	Power Electronic Controls of DC Drives (24211T03)	Digital Signal Processing (24211T04)	Design Control Drives (24211T06)	Research Methodology and IPR (24211T05)
M.Tech Electronics & Communication Engineering (VLSI & Embedded Systems)	Digital System Design (24411T01)	Embedded System Design (24411T02)	Analog VLSI Design (24411T03)	Programme Elective I System on Chip Design (24411T06)	Programme Elective II Soft Computing Techniques (24411T08)	Research Methodology & IPR (24411T12)
M.Tech Computer Science & Engineering (CSE)	Mathematical Foundations of Computer Science (24511T01)	Advanced Data structures & Algorithms (24511T02)	Machine Learning (24511T03)	Big Data Analytics (24511T04)	Object Oriented Software Engineering (24511T09)	Research Methodology and IPR (24511T10)

**NOTE:**

1. ANY OMISSIONS/ CLASHES / DISCREPANCIES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE COE, IMMEDIATELY.
2. ALL THE STUDENTS ARE ADVISED TO MAKE A NOTE OF THE EXAMINATION SCHEDULE.

**CONTROLLER OF EXAMINATIONS**

DATE:02-01-2025

The Controller of Examinations  
 DVR & Dr HS MIC College of Technology  
 Kanchikacherla - 521180 -



**PRINCIPAL**

PRINCIPAL  
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 Andhra Pradesh, India – Pin: 521180.