

DVR & Dr. HS

MIC College of Technology

(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)
Kanchikacherla - 521180, NTR Dist., A.P., India
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EXAMINATION SECTION - NOTIFICATION DATED : 02-01-2025

M Tech (I - Semester) - II Mid Examination Schedule(MIC24)- A.Y. 2024-25

2024 Admitted Batch

Timings: FN: 10:00AM To 12:00PM

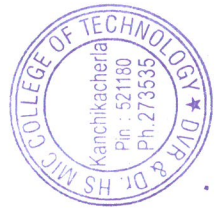
DATE & DAY	20-01-2025 (Monday)	21-01-2025 (Tuesday)	22-01-2025 (Wednesday)	23-01-2025 (Thursday)	24-01-2025 (Friday)	25-01-2025 (Saturday)
EEE (Power Electronics & Drives)	Mathematical Modeling of Machines (24211T01)	Analysis of Power Electronics (24211T02)	Power Electronic Controls of DC Drives (24211T03)	Digital Signal Processing (24211T04)	Design Control Drives (24211T06)	Research Methodology and IPR (24211T05)
ECE (VLSI & Embedded Systems)	Digital System Design (24411T01)	Embedded System Design (24411T02)	Analog VLSI Design (24411T03)	Programme Elective I System on Chip Design (24411T06)	Programme Elective II Soft Computing Techniques (24411T08)	Research Methodology and IPR (24411T12)
Computer Science & Engineering (CSE)	Mathematical Foundations of Computer Science (24511T01)	Advanced Data structures & Algorithms (24511T02)	Machine Learning (24511T03)	Big Data Analytics (24511T04)	Object Oriented Software Engineering (24511T09)	Research Methodology and IPR (24511T10)

CONTROLLER OF EXAMINATIONS

Copy to : 1.HOD-EEE/ECE/CSE(to be read in the M.Tech I sem classrooms)
2. Notice Board

The Controller of Examinations
DVR & Dr HS MIC College of Technology
Kanchikacherla - 521180 .


PRINCIPAL



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