

Date: 24.01.2025

External Lab (Regular) Schedule January- 2025

M.Tech (CSE) I Semester – MIC24

Date	Name of the Lab	09:00 – 12:00
28-01-2025	Advanced Data Structures & Algorithms Lab (24511L11)	ALL
29-01-2025	Advanced Computing Lab (24511L12)	ALL

Batch Members (10)

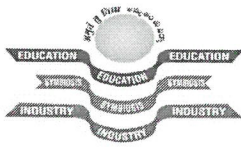
A: 24H71D5801 – 24H71D5810

(10)



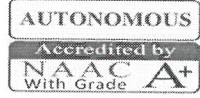
Chief Superintendent

CHIEF SUPERINTENDENT  
DVR & Dr. HS MIC College of Technology  
Kanchikacherla, N.T.R. Dt,  
Andhra Pradesh, India – Pin: 521180.



DVR & Dr. HS  
MIC College of Technology

ISO 9001:2015 Certified Institute  
(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)  
Kanchikacherla - 521180, NTR Dist., A.P., India  
Phones : 08678 - 273535 / 94914 57799 / 73826 16824  
E mail: office@mictech.ac.in, Website: www.mictech.edu.in



Date: 24.01.2025

**External Lab (Regular) Schedule January- 2025**

**M.Tech (ECE VLSI & Embedded Systems) I Semester – MIC24**

Date	Name of the Lab	09:00 – 12:00
28-01-2025	VLSI System Design Lab - I (24411L10)	ALL
29-01-2025	Digital System Design Using Verilog and VHDL (24411L11)	ALL

**Batch Members (05)**

A: 24H71D6801 – 24H71D6805

(05)

**Chief Superintendent**



CHIEF SUPERINTENDENT  
DVR & Dr. HS MIC College of Technology  
Kanchikacherla, N.T.R. Dt.  
Andhra Pradesh, India – Pin: 521180.