



B.Tech III - Semester- Supplementary(MIC18) Examinations, Apr-2025

TIME TABLE

TIME : 10:00 AM TO 1:00PM

BRANCH	03-04-2025 (Thursday)	07-04-2025 (Monday)	09-04-2025 (Wednesday)	11-04-2025 (Friday)	16-04-2025 (Wednesday)	19-04-2025 (Saturday)	21-04-2025 (Monday)
CIVIL ENGINEERING (01-CE)	Numerical Methods and Probability Statistics (18CE3T02)	Life Science (18CE3T03)	Building Materials and Construction (18CE3T04)	Strength of Materials-I (18CE3T05)	Effective Technical Communication (18CE3T01)	Fluid Mechanics (18CE3T06)	Surveying (18CE3T07)
ELECTRICAL AND ELECTRONICS ENGINEERING (02 - EEE)	Complex Variables and Numerical Methods (18EE3T01)	Electrical Circuit Analysis - II (18EE3T02)	Analog Electronics-I (18EE3T03)	Electrical Machines-I (18EE3T04)	Electromagnetic Fields (18EE3T05)	Basics of Mechanical Engineering (18EE3T06)	-
MECHANICAL ENGINEERING (03 - ME)	Complex Variables and Numerical Methods (18ME3T01)	Mechanics of Solids (18ME3T02)	Kinematics of Machines (18ME3T03)	Thermodynamics (18ME3T04)	Materials Science and Engineering (18ME3T05)	Fluid Mechanics & Hydraulic Machines (18ME3T06)	-
ELECTRONICS & COMMUNICATIONS ENGINEERING (04 - ECE)	Probability Theory & Stochastic Processes (18EC3T01)	Electronic Circuit Analysis (18EC3T02)	Digital Electronics (18EC3T03)	Network Theory (18EC3T04)	Signals & Systems (18EC3T05)	Managerial Economics and Financial Analysis (18EC3T06)	-
COMPUTER SCIENCE & ENGINEERING (05 - CSE)	Probability and Statistics (18CS3T01)	Object Oriented Programming (18CS3T02)	Advanced Data Structures (18CS3T03)	Digital Logic Design (18CS3T04)	Effective Technical Communication (18CS3T05)	-	-
INFORMATION TECHNOLOGY (12-IT)	Probability and Statistics (18IT3T01)	Object Oriented Programming (18IT3T02)	Advanced Data Structures (18IT3T03)	Digital Logic Design (18IT3T04)	Effective Technical Communication (18IT3T05)	-	-

NOTE:

1. ANY OMISSIONS/ CLASHES / DISCREPANCIES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE COE, IMMEDIATELY.
2. ALL THE STUDENTS ARE ADVISED TO MAKE A NOTE OF THE EXAMINATION SCHEDULE.

CONTROLLER OF EXAMINATIONS

DATE: 10-03-2025

10-8-10/3
The Controller of Examinations
DVR & Dr HS MIC College of Technology
Kanchikacherla - 521180 -



10/03/2025
PRINCIPAL
DVR & Dr. HS MIC College of Technology
Kanchikacherla, N.T.R. Dt.
Andhra Pradesh, India – Pin: 521180.