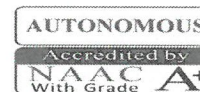




DVR & Dr. HS
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ISO 9001:2015 Certified Institute
(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)
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EXAMINATION SECTION - NOTIFICATION DATED : 07-04-2025

M Tech (II - Semester)- I Mid Examination Schedule(MIC24)- A.Y. 2024-25

2024 Admitted Batch

Timings: AN: 02:00PM To 04:00PM

DATE & DAY	15-04-2025 (Tuesday)	16-04-2025 (Wednesday)	17-04-2025 (Thursday)	19-04-2025 (Saturday)	21-04-2025 (Monday)	22-04-2025 (Tuesday)
EEE (Power Electronics & Drives)	Power Electronic Control of AC Drives (24212T01)	Advanced Power Electronic Converters (24212T02)	Professional Elective-I Renewable Energy Source (24212T03)	Professional Elective-II Flexible AC Transmission Systems (24212T07)	Professional Elective-III Embedded Systems (24212T09)	-
ECE (VLSI & Embedded Systems)	Embedded Real Time Operating Systems (24412T01)	Low Power VLSI Design (24412T02)	CMOS Mixed Signal Circuit Design (24412T03)	Programme Elective III Advanced Computer Architecture (24412T06)	Programme Elective IV Design of IOT (24412T07)	-
CSE (Computer Science & Engineering)	Deep Learning (24512T01)	MEAN Stack Technologies (24512T02)	Program Elective III Advanced Databases and Mining (24512T03)	Programme Elective IV Cloud Computing (24512T06)	Open Elective Digital Marketing (24512T10)	★ Disaster Management (24512M15)



Audit Course

CONTROLLER OF EXAMINATIONS

Copy to : 1.HOD-EEE,ECE &CSE (to be read in the M.Tech II sem classroom)
2. Notice Board

The Controller of Examinations
DVR & Dr HS MIC College of Technology
Kanchikacherla - 521180



PRINCIPAL

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Kanchikacherla, N.T.R. Dt.
Andhra Pradesh, India – Pin: 521180.