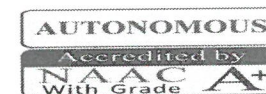




DVR & Dr. HS
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 ISO 9001:2015 Certified Institute
 (Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)
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M.Tech II - Semester- Regular (MIC24) Examinations, Jun 2025

TIME TABLE

TIME : 10:00 AM TO 1:00PM

| BRANCH | 30-06-2025 (Monday) | 02-07-2025 (Wednesday) | 04-07-2025 (Friday) | 07-07-2025 (Monday) | 09-07-2025 (Wednesday) |
|---|--|---|---|---|--|
| M.Tech Electrical & Electronics Engineering (Power Electronics & Drives) | Power Electronic Control of AC Drives (24212T01) | Advanced Power Electronic Converters (24212T02) | Professional Elective-I Renewable Energy Source (24212T03) | Professional Elective-II Flexible AC Transmission Systems (24212T07) | Professional Elective-III Embedded Systems (24212T09) |
| M.Tech Electronics & Communication Engineering (VLSI & Embedded Systems) | Embedded Real Time Operating Systems (24412T01) | Low Power VLSI Design (24412T02) | CMOS Mixed Signal Circuit Design (24412T03) | Programme Elective III Advanced Computer Architecture (24412T06) | Programme Elective IV Design of IOT (24412T07) |
| M.Tech Computer Science & Engineering (CSE) | Deep Learning (24512T01) | MEAN Stack Technologies (24512T02) | Program Elective III Advanced Databases and Mining (24512T03) | Programme Elective IV Cloud Computing (24512T06) | Open Elective Digital Marketing (24512T10) |

NOTE:

1. ANY OMISSIONS/ CLASHES / DISCREPANCIES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE COE, IMMEDIATELY.
2. ALL THE STUDENTS ARE ADVISED TO MAKE A NOTE OF THE EXAMINATION SCHEDULE.

CONTROLLER OF EXAMINATIONS

DATE: 10-06-2025

The Controller of Examinations
 DVR & Dr HS MIC College of Technology
 Kanchikacherla - 521180 -



PRINCIPAL
PRINCIPAL

DVR & Dr. HS MIC College of Technology
 Kanchikacherla, N.T.R. Dt.
 Andhra Pradesh, India – Pin: 521180.