



DVR & Dr. HS
MIC College of Technology
ISO 9001:2015 Certified Institute
(Approved by AICTE & Permanently Affiliated to JNTU/K, Kakinada)
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Date: 18-06-2025

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

External Lab (Regular) Schedule Jun 2025

M.Tech (VLSI&ES) - II Semester – MIC24

Date	Name of the Lab	09:00–12:00
23-06-2025	Embedded System Design Lab – 24412L10	ALL
24-06-2025	VLSI System Design Lab - II– 24412L11	ALL

Batch Members (5) : 24H71D6801 to 24H71D6805




Chief Superintendent

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

External Lab (Regular) Schedule Jun 2025

M.Tech (CSE) II Semester – MIC24

Date	Name of the Lab	09:00–12:00
23-06-2025	Machine Learning with Python Lab– 24512L12	ALL
24-06-2025	MEAN Stack Technologies Lab– 24512L13	ALL

Batch Members (10) : 24H71D5801 to 24H71D5810




Chief Superintendent

CHIEF SUPERINTENDENT
DVR & Dr. HS MIC College of Technology
Kanchikacherla, N.T.R. Dt.
Andhra Pradesh, India – Pin: 521180.