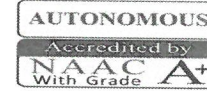


DVR & Dr. HS
MIC College of Technology
ISO 9001:2015 Certified Institute
(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)
Kanchikacherla - 521180, NTR Dist., A.P., India
Phones : 08678 - 273535 / 94914 57799 / 73826 16824
E mail: office@mictech.ac.in, Website: www.mictech.edu.in



EXAMINATION SECTION - NOTIFICATION DATED : 30-01-2026

M Tech (I - Semester)- II Mid Examination Schedule(MIC25)- A.Y. 2025-26

2025 Admitted Batch

Timings: AN: 02:00PM To 04:00PM

DATE & DAY	16-02-2026 (Monday)	17-02-2026 (Tuesday)	18-02-2026 (Wednesday)	19-02-2026 (Thursday)	20-02-2026 (Friday)
ECE (VLSI & Embedded Systems)	Advanced Digital Systems Design (D2516801)	Embedded Hardware Platforms and Programming (D2516802)	FPGA Design (D2516803)	<u>Programme Elective I</u> VLSI System Design (D25168A2)	<u>Programme Elective II</u> System on Programming chip Design (D25168B0)
CSE (Computer Science & Engineering)	Data Structures and Algorithm Analysis (D2515801)	Advanced Data Warehousing and Data Mining (D2515802)	Mathematical Foundations of Computer Science (D2515803)	Advanced Computer Networks (D25158A2)	Agile Methodologies (D25158B3)


CONTROLLER OF EXAMINATIONS

Copy to : 1.HOD-ECE & CSE (to be read in the M.Tech I sem classroom)
2. Notice Board




PRINCIPAL

Principal
DVR & Dr. HS MIC College of Technology
Kanchikacherla, NTR District